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(6 citations)

University of Toronto. Region-Oriented Main **Memory Management** in Shared-Memory NUMA Multiprocessors
ftp.cs.toronto.edu/pub/parallel/Gamsa_MASc.ps.Z

Java Operating Systems: Design and Implementation - Back, Tullmann, Stoller.. (1998) (Correct) (38 citations)
systems such as Java use type safety to provide **memory** safety in a single address space. **Memory** safety
a process model that enables the control and **management** of computational resources. In particular,
mancos.cs.utah.edu/papers/javaos-tr98015.ps.gz

Cyclic Weighted Reference Counting without Delay - Jones, Lins (1992) (Correct) (14 citations)
can be performed simultaneously. Keywords: **Memory management**, Distributed **memory**, Reference
be performed simultaneously. Keywords: **Memory management**, Distributed **memory**, Reference counting,
unix.hensa.ac.uk/pub/misc/ukc.reports/comp.sci/reports/28-92.ps.Z

Networking Implementation Notes 4.BSD Edition - Leffler, Joy, Fabry, Karels (Correct)
3. Goals 4. Internal address representation 5. **Memory management** 6. Internal layering 6.1. Socket
4. Internal address representation 5. **Memory management** 6. Internal layering 6.1. Socket layer 6.1.1.
ftp.riken.go.jp/pub/NetBSD/misc/lite2-docs/smm/18.net.ps.gz

VINO: An Integrated Platform for Operating System and.. - Small, Seltzer (1994) (Correct) (17 citations)
control of the DBMS. However, because the virtual **memory** of the database process is still managed by the
of traditional and modern database **management** systems. 1 Introduction In general, operating
hpc.ee.ntu.edu.tw/~murphy/reports/rtos/papers/vino-tr-30-94.ps.gz

Low Power TLB Design for High Performance Microprocessors - Manne, Klauser, Grunwald.. (1997) (Correct) (4 citations)
Much of the energy consumption comes from the **memory** hierarchy of the processor. Previous studies [10]
space protection and flexibility in **memory management**. In operating systems supporting
bessie.colorado.edu/personal/bobbie/Papers/islped97.ps

Mach: A Foundation for Open Systems - Rashid, Baron, Forin, Golub.. (1989) (Correct) (40 citations)
the **management** of CPU, communication, virtual **memory** and secondary storage resources in a way that
system and user applications. It exposes the **management** of CPU, communication, virtual **memory** and
ftp.cs.cmu.edu/project/mach/doc/published/intro.ps

Cooperative Caching in Append-only Databases with Hot Spots - Aman Sinha (Correct)
as follows: Section 2 discusses work related to **memory management** in both the operating systems and
server? We describe and compare several cache **management** policies to study these issues. In each case,
lore.ece.utexas.edu/~sinha/ICDE.ps

Tempest and Typhoon: User-Level Shared Memory - Reinhardt, Larus, Wood (1994) (Correct) (243 citations)
Tempest and Typhoon: User-Level Shared **Memory** Steven K. Reinhardt, James R. Larus, and David A.
ftp.cs.wisc.edu/wwwt/isca94_typhoon.ps

From Region Inference to von Neumann Machines via.. - Birkedal, Tofte.. (1996) (Correct) (73 citations)
calculus can be implemented using regions for **memory management**[17]At runtime, the store consists of
can be implemented using regions for **memory management**[17]At runtime, the store consists of a stack
ftp.diku.dk/diku/semantics/papers/D-313.ps.gz

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[Bus Minimization and Scheduling of Multi-Chip Systems - Sheliga, Sha](#) (Correct)

Abstract This paper considers several different **algorithms** that reduce the required number of buses for module design. An efficient polynomial time **algorithm** that calculates the minimum number of buses schedule is presented. We also present three **algorithms** that minimize the number of buses during
www.nd.edu/~esha/papers/mike/bus_gls6.ps

[Run-Time Parallelization Of Irregular Doacross Loops - Jeyaraman, Krothapalli.. \(1996\)](#) (Correct)

presented the performance of the **algorithm** on a **collection** of distributed networked heterogeneous to perform a run-time analysis. We describe a new **algorithm** to perform this analysis. The proposed method to design effective run-time parallelization **algorithms** [2,3,4,5,6,7,8,9,10]The main differences
ftp.csd.uwo.ca/pub/mwg/doacross.ps

[Model Reduction from an H1/LMI perspective - Helmersson](#) (Correct)

norm reduction. As an intermediate step, the **algorithm** finds a generalized balanced realization such and Hankel norm reduction [6] are well-behaved **algorithms** for finding reduced order models. In this paper cases (if $k \in \Gamma$)The model reduction **algorithm** proposed is based on an iterative two-step LMI
ftp.control.isy.liu.se/pub/Reports/1994/1690.ps.Z

[Parallel Algorithms for High-dimensional Proximity Joins - Shafer, Agrawal \(1997\)](#) (Correct) (4 citations)

Parallel **Algorithms** for High-dimensional Proximity Joins John C.

We present a parallel multidimensional join **algorithm** based on an the epsilon-kdB tree and compare it Of Space Partitioning. An Evaluation Of The **Algorithms** On An Ibm Sp2 Shared-Nothing Multiprocessor Is
www.almaden.ibm.com/cs/people/ragrawal/papers/vldb97_ekdb.ps

[Calculation of Zero Dynamics using the Ritt Algorithm - Fortell \(1994\)](#) (Correct)

Calculation of Zero Dynamics using the Ritt **Algorithm** Hakan Fortell Department of Electrical how differential algebra, in particular the Ritt **algorithm**, can be used to calculate zero dynamics of a that for affine polynomial SISO systems the Ritt **algorithm** gives a result which is equivalent to the
ftp.control.isy.liu.se/pub/Reports/1994/1585.ps.Z

[Porting the Galaxy System to Mandarin Chinese - Wang \(1997\)](#) (Correct) (2 citations)

Pao, and Ed Hurley, for their assistance in data **collection** and maintaining the system Vicky Palay and . 50 3.2.3 City-state Decoding **Algorithm** .52 3.3 . 38 2-9 **Algorithm** to correct type 2 homophones .
www.sls.lcs.mit.edu/sls/publications/1997/synthesis-wangc.ps.gz

[Sparse coding with an overcomplete basis set: A strategy.. - Olshausen, Field \(1998\)](#) (Correct) (103 citations)

so that images may be explained in terms of a **collection** of independent events. The hope is that such a the simulation and results obtained applying the **algorithm** to natural images are described in Sections 6 the model, as well as the relation between our **algorithm** and other efficient coding methods that have
redwood.ucdavis.edu/pub/papers/VR.ps.Z

[Analysis of Approximate Nearest Neighbor Searching with.. - Maneewongvatana, Mount \(1999\)](#) (Correct) (1 citation)

is given not only the data points, but also a **collection** of sample query points, called the training with dimension. The difficulty of obtaining **algorithms** that are efficient in the worst case with been heavily studied recently. Examples include **algorithms** by Bern [8]Arya and Mount [2]Arya, et al.
ftp.cs.umd.edu/pub/faculty/mount/Papers/dimacs99.ps.gz

[Comprehension Syntax - Buneman, Libkin, Suciu, Tannen, Wong \(1994\)](#) (Correct) (42 citations)

syntax that deals uniformly with a variety of **collection** types it also includes pattern matching,

[29] D. Suciu and J. Paredaens. Any **Algorithm** in the Complex Object Algebra Needs 1991. 34] A. Wijngaarden. Revised Report on the **Algorithmic** Language ALGOL 68. Acta Informatica, sdmc.krdl.org.sg/kleisli/psZ/blstwsigmodrecord94.ps

A Statically Safe Alternative to Virtual Types - Bruce, Odersky, Wadler (1998) (Correct) (33 citations)
Parametric types are especially useful for **collection** types, such as lists or sets. Users of C
www.cs.bell-labs.com/~wadler/topics/./papers/parvsvirt/parvsvirt.ps.gz

A Temporal Extension to a Generic Object Data Model - Steiner, Norrie (1997) (Correct) (1 citation)
are represented by the bulk type constructor **collection** and classification structures are built from
www.timeconsult.com/Literature/tom.ps

I/O Optimal Isosurface Extraction (Extended Abstract) - Chiang, Silva (Correct)
[9] propose a method based on identifying a **collection** of seed cells from which isosurfaces can be
grids. We show that, in practice, our **algorithms** improve the performance of isosurface
depending on the type of cells, one can apply an **algorithm** to actually generate the isosurface from those
cis.poly.edu/chiang/iso-vis97.ps.gz

Optimized Software Synthesis for Digital Signal.. - Jürgen Teich.. (1998) (Correct) (3 citations)
relative to the Evolutionary **Algorithm** on our **collection** of CHAPTER 4. EXPERIMENTS 23 1500 Fitness
Software Synthesis for Digital Signal Processing **Algorithms** -An Evolutionary Approach Jurgen Teich and
.9 2.2 Why Using an Evolutionary **Algorithm** .10 2.3
ftp.tik.ee.ethz.ch/pub/people/zitzler/TZB1998a.ps.gz

Learning in Case-Based Classification Algorithms - Globig, Wess (1995) (Correct) (1 citation)
Learning in Case-Based Classification **Algorithms** Christoph Globig, Stefan Wess University of
To do so, we transform a simple symbolic learning **algorithm** (the version space **algorithm**) into an
symbolic learning **algorithm** (the version space **algorithm**) into an equivalent case-based variant. The
kbibmp3.ub.uni-kl.de/Preprint_Informatik/PS/no_series_174.ps.gz

Color-Based Content Coding with Applications to Sign.. - Schumeyer, Barner (1997) (Correct)
that encompass these features. Two segmentation **algorithms** are presented: the first uses a static region
and hands in real-time. The dynamic segmentation **algorithm** identifies flesh regions using statistical
at a the same bit rate when compared to a uniform **algorithm**. Permission to publish this abstract separately
www.asel.udel.edu/sem/research/speech/tcsvt.ps.gz

A Global Convergence Theory for Sequential Linear Programming.. - Hallabi (1994) (Correct)
for Sequential Linear Programming Inexact Hybrid **Algorithms** Mohammadi El Hallabi CRPC-TR94371 January
1994
Convergence Theory for SLP and SQP Trust-Region **Algorithms**. Also available as CAAM-TR95-08 from the
For Sequential Linear Programming Inexact Hybrid **Algorithms** 1 Mohammadi El Hallabi 2 Abstract. In This
softlib.rice.edu/pub/CRPC-TRs/reports/CRPC-TR94371.ps.gz

On Lazy Randomized Incremental Construction - de Berg, Dobrindt, Schwarzkopf (1995) (Correct) (9 citations)
in the plane. For every subset $R \subseteq S$, define a **collection** of "regions" $C(R)$ The set of line segments R
introduce a new type of randomized incremental **algorithms**. Contrary to standard randomized incremental
Contrary to standard randomized incremental **algorithms**, these lazy randomized incremental **algorithms**
www.cs.ust.hk/~otfried/Papers/1994-12.ps

Strategic Oscillation in Heuristic Local Search - Laurent Mynard (Correct)
Abstract This paper describes a local search **algorithm** for combinatorial optimization problems. It is
knapsack problem. Experience proves that the **algorithm** is very efficient, as well according to the
based on three main technics. Firstly specific **algorithms** are designed to solve one given problem by
www.poleia.lip6.fr/~mynard/frames/./ps/kbcs96.ps.gz

Automatic Configuration of Parallel Programs for.. - Hluchý, Dobrovodský.. (Correct)
Abstract: This paper describes the mapping **algorithm** for distributed memory, parallel message
is duration of the iteration step of the mapping **algorithm**. $Fvertex(M,t)$ expresses the effect of
influence of communications becomes weaker. The **algorithm** can be implemented in the distributed
ups.savba.sk/parcom/sephp/docs/parco4r.ps.gz

PSATO: a Distributed Propositional Prover and Its.. - Zhang, Bonacina, Hsiang (1996) (Correct) (9 citations)
Stinson, D. eds)Contemporary Design Theory: A **Collection** of Surveys. Boehm, M.Speckenmeyer, E. 1994)
an efficient implementation of the Davis-Putnam **algorithm**. The masterslave model is used for
(ii) designing highly scalable parallel **algorithms** and (iii) supporting "fault-tolerant"
www.cs.uiowa.edu/ftp/hzhang/sato/papers/jscpsato.ps.Z

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using a specialized library, namely PVM (Parallel **Virtual Machine**) An example is given :the LBG specialized library, namely PVM (Parallel **Virtual Machine**) An example is given :the LBG **algorithm** for efficiently with time-consuming image processing **algorithms**. It is shown how to distribute the processes, ns1.tele.ucl.ac.be/PEOPLE/PP/lbgparal.ps.gz

[A Syntactic Framework For Bitstream-Level Representation Of.. - Yihan Fang \(1996\) \(Correct\)](#)

languages using **virtual machines**, most notably Java [7, 8] the language would have the advantage of coding. It should be compile source code **virtual machine** code **virtual machine** interpreter and thoroughly defined semantics suitable for **machine** translation. This work is currently part of the www.cnmtc.columbia.edu/~eleft/papers/icip96-yf.ps.gz

[PVM-Prolog: Parallel Logic Programming in the PVM System - Rui Marques \(1995\) \(Correct\) \(2 citations\)](#)PVM Interface Layer 7 4.1 Accessing a parallel **virtual machine** from Prolog :7 4.2Layer 7 4.1 Accessing a parallel **virtual machine** from Prolog :7 4.2 Summary of

www.soi.city.ac.uk/~msch/pvm/marques.ps.gz

[Composition Validation and Subjectivity in GenVoca Generators - Batory, Geraci \(1997\) \(Correct\) \(30 citations\)](#)

defined by a series of progressively more abstract **virtual machines** [Dij68] A **virtual machine** is a set of a series of progressively more abstract **virtual machines** [Dij68] A **virtual machine** is a set of classes, present simple, efficient, and domainindependent **algorithms** for validating compositions of GenVoca ftp.cs.utexas.edu/pub/predator/ieee-icsr.ps

[On The Granularity And Clustering Of Directed Acyclic Task.. - Gerasoulis, Yang \(1990\) \(Correct\) \(47 citations\)](#)

number of processors on a completely connected **virtual** architecture. Sarkar calls this step the Task Scheduling over Distributed Memory **Machines**, Proc. of the International Workshop on Parallel with a performance bound for linear clustering **algorithms**, shows that linear clustering is the best www.cs.rutgers.edu/pub/gerasoulis/reports/LCSR-TR-153.ps.Z

[End-to-End Delay Bounds and Buffer Sizing in ATM Networks - Hung And \(1995\) \(Correct\) \(1 citation\)](#)

we obtain end-to-end delay bounds over arbitrary **virtual** circuits of such schedulers. For (oe cheetah.vlsi.uwaterloo.ca/~kesidis/End-to-End.ps

[The VEOS Project - Bricken, Coco \(1993\) \(Correct\) \(14 citations\)](#)

it creates with its own interface. One solution, **virtual** reality (VR) immediately raises fundamental www.hitl.washington.edu/publications/r-93-3//r-93-3.ps

[CalREN ATM testbed Deployment: A Report - Senthil Sengodan \(Correct\)](#)

the user different bit rates of transport. 2.2 **Virtual** Path and **Virtual** Channel Since ATM is the demonstrations to connect to SGI Indigo **machines** at the WBIT booth. The ASX-100 can support both commsci.usc.edu/~sengodan/pubs/calren.ps

[STALK: An Interactive Virtual Molecular Docking System - Levine, Facello.. \(1996\) \(Correct\)](#)STALK: An Interactive **Virtual** Molecular Docking System David Levine

x Abstract Several recent technologies-genetic **algorithms**, parallel and distributed computing, **virtual** of molecular interactions. Parallel genetic **algorithms** are an efficient and effective means to explore info.mcs.anl.gov/pub/tech_reports/reports/P603.ps.Z

[Supporting Transcontinental Collaborative Work in Persistent.. - Leigh, Johnson \(1996\) \(Correct\) \(6 citations\)](#)Transcontinental Collaborative Work in Persistent **Virtual** Environments Jason Leigh and Andrew E. Johnson

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IEEE STD IEEE Standard

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Peng-Yeng Yin; Bhanu, B.; Kuang-Cheng Chang; Anlei Dong;
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Digital Object Identifier 10.1109/TPAMI.2005.201
[AbstractPlus](#) | Full Text: [PDF](#)(1872 KB) IEEE JNL
- ☐ 2. Q-learning-based multirate transmission control scheme for RRM in mult systems
Yih-Shen Chen; Chung-Ju Chang; Fang-Chin Ren;
Vehicular Technology, IEEE Transactions on
Volume 53, Issue 1, Jan. 2004 Page(s):38 - 48
Digital Object Identifier 10.1109/TVT.2003.822330
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(360 KB) IEEE JNL
- ☐ 3. Q-DPM: an efficient model-free dynamic power management technique
Min Li; Xiaobo Wu; Yao, R.; Xiaolang Yan;
Design, Automation and Test in Europe, 2005. Proceedings
2005 Page(s):526 - 527 Vol. 1
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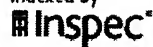
IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. Progress in learning 3 vs. 2 keepaway
 Kuhlmann, G.; Stone, P.;
 Systems, Man and Cybernetics, 2003. IEEE International Conference on
 Volume 1, 5-8 Oct. 2003 Page(s):52 - 59 vol.1
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Rezaei, M.; Kavi, K.M.;
Southeastcon 2000. Proceedings of the IEEE
7-9 April 2000 Page(s):332 - 339
Digital Object Identifier 10.1109/SECON.2000.845587
AbstractPlus Full Text: PDF (548 KB) IEEE CNF |
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Object-Oriented Real-Time Distributed Computing, 2005. ISORC 2005. Eighth International Symposium on
18-20 May 2005 Page(s):382 - 389
Digital Object Identifier 10.1109/ISORC.2005.61
AbstractPlus Full Text: PDF (192 KB) IEEE CNF |
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Chang, J.M.; Srisa-An, W.; Chia-Tien Dan Lo;
High Performance Computing in the Asia-Pacific Region, 2000. Proceedings. 1 International Conference/Exhibition on
Volume 1, 14-17 May 2000 Page(s):513 - 517 vol.1
Digital Object Identifier 10.1109/HPC.2000.846607
AbstractPlus Full Text: PDF (396 KB) IEEE CNF |
| <input type="checkbox"/> | 4. New coding patterns for object management in C++
Dingle, A.; Hildebrandt, T.H.;
Technology of Object-Oriented Languages and Systems, 1997. TOOLS 23. Pr
28 July-1 Aug. 1997 Page(s):38 - 47
Digital Object Identifier 10.1109/TOOLS.1997.654699
AbstractPlus Full Text: PDF (52 KB) IEEE CNF |
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Nilsen, K.D.; Hong Gao;
Real-Time Technology and Applications Symposium, 1995. Proceedings
15-17 May 1995 Page(s):142 - 153
Digital Object Identifier 10.1109/RTTAS.1995.516211
AbstractPlus Full Text: PDF (1136 KB) IEEE CNF |

- ☐ **6. HeapGuard, eliminating garbage collection in real-time Ada systems**
Harbaugh, S.; Wavering, B.;
Aerospace and Electronics Conference, 1991. NAECON 1991., Proceedings of
National
20-24 May 1991 Page(s):704 - 708 vol.2
Digital Object Identifier 10.1109/NAECON.1991.165829
[AbstractPlus](#) | Full Text: [PDF](#)(360 KB) IEEE CNF

- ☐ **7. Lock-free garbage collection for multiprocessors**
Herlihy, M.P.; Moss, J.E.B.;
Parallel and Distributed Systems, IEEE Transactions on
Volume 3, Issue 3, May 1992 Page(s):304 - 311
Digital Object Identifier 10.1109/71.139204
[AbstractPlus](#) | Full Text: [PDF](#)(732 KB) IEEE JNL

- ☐ **8. Active memory processor: a hardware garbage collector for real-time Java devices**
Srisa-an, W.; Lo, C.-T.D.; Chang, J.-M.;
Mobile Computing, IEEE Transactions on
Volume 2, Issue 2, Apr-Jun 2003 Page(s):89 - 101
Digital Object Identifier 10.1109/TMC.2003.1217230
[AbstractPlus](#) | Full Text: [PDF](#)(3797 KB) IEEE JNL

- ☐ **9. Complete distributed garbage collection: an experience with Rotor**
Veiga, L.; Ferreira, P.;
Software, IEEE Proceedings- [see also Software Engineering, IEEE Proceedings]
Volume 150, Issue 5, 27 Oct. 2003 Page(s):283 - 290
Digital Object Identifier 10.1049/ip-sen:20030993
[AbstractPlus](#) | Full Text: [PDF](#)(277 KB) IEEE JNL

- ☐ **10. Research and analysis of garbage collection mechanism for real-time embedded systems**
Liu Wei; Chen Zhang-long; Tu Shi-hang;
Computer Supported Cooperative Work in Design, 2004. Proceedings. The 8th
Conference on
Volume 1, 26-28 May 2004 Page(s):462 - 468 Vol.1
[AbstractPlus](#) | Full Text: [PDF](#)(675 KB) IEEE CNF

- ☐ **11. The object behavior of Java object-oriented database management systems**
Lo, C.-T.D.; Chang, M.; Frieder, O.; Grossman, D.;
Information Technology: Coding and Computing, 2002. Proceedings. International
on
8-10 April 2002 Page(s):247 - 252
Digital Object Identifier 10.1109/ITCC.2002.1000395
[AbstractPlus](#) | Full Text: [PDF](#)(291 KB) IEEE CNF

- ☐ **12. A study on a garbage collector for embedded applications**
Krapf, R.C.; de Mattos, J.C.B.; Spellmeier, G.; Carro, L.;
Integrated Circuits and Systems Design, 2002. Proceedings. 15th Symposium
9-14 Sept. 2002 Page(s):127 - 132
Digital Object Identifier 10.1109/SBCCI.2002.1137648
[AbstractPlus](#) | Full Text: [PDF](#)(251 KB) IEEE CNF

- ☐ **13. An analysis of the garbage collection performance in Sun's HotSpot™ JVM**
Dykstra, L.; Srisa-an, W.; Chang, J.M.;
Performance, Computing, and Communications Conference, 2002. 21st IEEE
3-5 April 2002 Page(s):335 - 339
Digital Object Identifier 10.1109/IPCCC.2002.995167

[AbstractPlus](#) | Full Text: [PDF](#)(563 KB) IEEE CNF

- ☐ **14. A performance comparison between stop-the-world and multithreaded concurrent generational garbage collection for Java**
Lo, C.-T.D.; Srisa-an, W.; Chang, J.M.;
Performance, Computing, and Communications Conference, 2002. 21st IEEE
3-5 April 2002 Page(s):301 - 308
Digital Object Identifier 10.1109/IPCCC.2002.995163

[AbstractPlus](#) | Full Text: [PDF](#)(748 KB) IEEE CNF

- ☐ **15. A performance analysis of the active memory system**
Witawas Srisa-An; Srisa-an; Chia-Tien Dan Lo; J Morris Chang;
Computer Design, 2001. ICCD 2001. Proceedings. 2001 International Conference
23-26 Sept. 2001 Page(s):493 - 496
Digital Object Identifier 10.1109/ICCD.2001.955073

[AbstractPlus](#) | Full Text: [PDF](#)(344 KB) IEEE CNF

- ☐ **16. Bounding worst case garbage collection time for embedded real-time systems**
Taehyoun Kim; Naehyuck Chang; Heonshik Shin;
Real-Time Technology and Applications Symposium, 2000. RTAS 2000. Proceedings.
IEEE
31 May-2 June 2000 Page(s):46 - 55
Digital Object Identifier 10.1109/RTAS.2000.852450

[AbstractPlus](#) | Full Text: [PDF](#)(312 KB) IEEE CNF

- ☐ **17. Conservative garbage collection on distributed shared memory systems**
Weimin Yu; Cox, A.;
Distributed Computing Systems, 1996., Proceedings of the 16th International Conference
27-30 May 1996 Page(s):402 - 410
Digital Object Identifier 10.1109/ICDCS.1996.507988

[AbstractPlus](#) | Full Text: [PDF](#)(820 KB) IEEE CNF

- ☐ **18. The case for Java as a programming language**
Van Hoff, A.;
Internet Computing, IEEE
Volume 1, Issue 1, Jan.-Feb. 1997 Page(s):51 - 56
Digital Object Identifier 10.1109/4236.585172

[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(200 KB) IEEE JNL

- ☐ **19. Supporting several real-time applications on the Java platform**
Higuera-Toledano, M.T.;
Computer Systems and Applications, 2005. The 3rd ACS/IEEE International Conference
2005 Page(s):142
Digital Object Identifier 10.1109/AICCSA.2005.1387131

[AbstractPlus](#) | Full Text: [PDF](#)(1075 KB) IEEE CNF

- ☐ **20. A new task scheduling method for distributed programs which require memory management in grids**
Koide, H.; Oie, Y.;
Applications and the Internet Workshops, 2004. SAINT 2004 Workshops. 2004
Symposium on
26-30 Jan. 2004 Page(s):666 - 673
Digital Object Identifier 10.1109/SAINTW.2004.1268708

[AbstractPlus](#) | Full Text: [PDF](#)(389 KB) IEEE CNF

- ☐ **21. Dynamic management of nursery space organization in generational collection**
Velasco, J.M.; Ortiz, A.; Olcoz, K.; Tirado, F.;
Interaction between Compilers and Computer Architectures, 2004. INTERACT

Workshop on
15 Feb. 2004 Page(s):33 - 40
Digital Object Identifier 10.1109/INTERA.2004.1299508
[AbstractPlus](#) | Full Text: [PDF](#)(1518 KB) IEEE CNF

- ☐ **22. Garbage collector refinement for new dynamic multimedia applications o systems**
Velasco, J.M.; Atienza, D.; Catthoor, F.; Tirado, F.; Olcoz, K.; Mendias, J.M.;
Interaction between Compilers and Computer Architectures, 2004. INTERACT
Workshop on
15 Feb. 2004 Page(s):25 - 32
Digital Object Identifier 10.1109/INTERA.2004.1299507
[AbstractPlus](#) | Full Text: [PDF](#)(1425 KB) IEEE CNF

- ☐ **23. Oil and water? High performance garbage collection in Java with MMTk**
Blackburn, S.M.; Cheng, P.; McKinley, K.S.;
Software Engineering, 2004. ICSE 2004. Proceedings. 26th International Conf
23-28 May 2004 Page(s):137 - 146
[AbstractPlus](#) | Full Text: [PDF](#)(328 KB) IEEE CNF

- ☐ **24. Implementing and optimizing real-time Java**
Corsaro, A.; Cytron, R.K.;
Parallel and Distributed Processing Symposium, 2003. Proceedings. Internatic
22-26 April 2003 Page(s):4 pp.
Digital Object Identifier 10.1109/IPDPS.2003.1213228
[AbstractPlus](#) | Full Text: [PDF](#)(189 KB) IEEE CNF

- ☐ **25. The design of a self-maintained memory module for real-time systems**
Chia-Tien Dan Lo;
System-on-Chip for Real-Time Applications, 2003. Proceedings. The 3rd IEEE
Workshop on
30 June-2 July 2003 Page(s):337 - 342
Digital Object Identifier 10.1109/IWSOC.2003.1213059
[AbstractPlus](#) | Full Text: [PDF](#)(270 KB) IEEE CNF

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View: 1-25 | [26-5](#)

- ☐ 1. **Region-based memory management for real-time Java**
 Higuera, T.; Issarny, V.; Banatre, M.; Cabillic, G.; Lesot, J.-P.; Parain, F.;
 Object-Oriented Real-Time Distributed Computing, 2001. ISORC - 2001. Proc
 IEEE International Symposium on
 2-4 May 2001 Page(s):387 - 394
 Digital Object Identifier 10.1109/ISORC.2001.922863
[AbstractPlus](#) | Full Text: [PDF](#)(692 KB) IEEE CNF
- ☐ 2. **Dynamic memory management for real-time embedded Java chips**
 Chi-Min Lin; Tien-Fu Chen;
 Real-Time Computing Systems and Applications, 2000. Proceedings. Seventh
 Conference on
 12-14 Dec. 2000 Page(s):49 - 56
 Digital Object Identifier 10.1109/RTCSA.2000.896370
[AbstractPlus](#) | Full Text: [PDF](#)(656 KB) IEEE CNF
- ☐ 3. **Evaluating the importance of virtual memory for Java**
 Becerra, Y.; Cortes, T.; Garcia, J.; Navarro, N.;
 Performance Analysis of Systems and Software, 2003. ISPASS. 2003 IEEE Int
 Symposium on
 6-8 March 2003 Page(s):101 - 110
[AbstractPlus](#) | Full Text: [PDF](#)(743 KB) IEEE CNF
- ☐ 4. **Object allocation and memory contention study of Java multithreaded ap**
 Wei Huang; Yang Qian; Srisa-an, W.; Chang, J.M.;
 Performance, Computing, and Communications, 2004 IEEE International Conf
 2004 Page(s):375 - 382
 Digital Object Identifier 10.1109/PCCC.2004.1395032
[AbstractPlus](#) | Full Text: [PDF](#)(891 KB) IEEE CNF
- ☐ 5. **Patterns and tools for achieving predictability and performance with real**
 Raman, K.; Yue Zhang; Panahi, M.; Colmenares, J.A.; Klefsad, R.;
 Embedded and Real-Time Computing Systems and Applications, 2005. Proce
 International Conference on
 17-19 Aug. 2005 Page(s):247 - 253
 Digital Object Identifier 10.1109/RTCSA.2005.68
[AbstractPlus](#) | Full Text: [PDF](#)(240 KB) IEEE CNF

- ☐ **6. Real-time Java scoped memory: design patterns and semantics**
Pizlo, F.; Fox, J.M.; Holmes, D.; Vitek, J.;
Object-Oriented Real-Time Distributed Computing, 2004. Proceedings. Seventh International Symposium on
2004 Page(s):101 - 110
Digital Object Identifier 10.1109/ISORC.2004.1300335
[AbstractPlus](#) | Full Text: [PDF](#)(1460 KB) IEEE CNF

- ☐ **7. A new task scheduling method for distributed programs which require m management in grids**
Koide, H.; Oie, Y.;
Applications and the Internet Workshops, 2004. SAINT 2004 Workshops. 2004 Symposium on
26-30 Jan. 2004 Page(s):666 - 673
Digital Object Identifier 10.1109/SAINTW.2004.1268708
[AbstractPlus](#) | Full Text: [PDF](#)(389 KB) IEEE CNF

- ☐ **8. A reconfigurable memory management core for Java applications**
Ejnoui, A.; Rhiati, A.;
VLSI, 2004. Proceedings. IEEE Computer society Annual Symposium on
19-20 Feb. 2004 Page(s):309 - 312
[AbstractPlus](#) | Full Text: [PDF](#)(265 KB) IEEE CNF

- ☐ **9. Towards the integration of scoped memory in distributed real-time Java**
Basanta-Val, P.; Garcia-Valls, M.; Estevez-Ayres, I.;
Object-Oriented Real-Time Distributed Computing, 2005. ISORC 2005. Eighth International Symposium on
18-20 May 2005 Page(s):382 - 389
Digital Object Identifier 10.1109/ISORC.2005.61
[AbstractPlus](#) | Full Text: [PDF](#)(192 KB) IEEE CNF

- ☐ **10. On real-time performance of ahead-of-time compiled Java**
Nilsson, A.; Robertz, S.G.;
Object-Oriented Real-Time Distributed Computing, 2005. ISORC 2005. Eighth International Symposium on
18-20 May 2005 Page(s):372 - 381
Digital Object Identifier 10.1109/ISORC.2005.38
[AbstractPlus](#) | Full Text: [PDF](#)(320 KB) IEEE CNF

- ☐ **11. Java-XCOM component integration on Linux operating system**
Mutlu, A.; Ege, M.;
Euromicro Conference, 2003. Proceedings. 29th
1-6 Sept. 2003 Page(s):70 - 75
[AbstractPlus](#) | Full Text: [PDF](#)(230 KB) IEEE CNF

- ☐ **12. Active memory processor: a hardware garbage collector for real-time Java devices**
Srisa-an, W.; Lo, C.-T.D.; Chang, J.-M.;
Mobile Computing, IEEE Transactions on
Volume 2, Issue 2, Apr-Jun 2003 Page(s):89 - 101
Digital Object Identifier 10.1109/TMC.2003.1217230
[AbstractPlus](#) | Full Text: [PDF](#)(3797 KB) IEEE JNL

- ☐ **13. Supporting several real-time applications on the Java platform**
Higuera-Toledano, M.T.;
Computer Systems and Applications, 2005. The 3rd ACS/IEEE International C
2005 Page(s):142
Digital Object Identifier 10.1109/AICCSA.2005.1387131

[AbstractPlus](#) | Full Text: [PDF](#)(1075 KB) IEEE CNF

- ☐ **14. Research and analysis of garbage collection mechanism for real-time em**
Liu Wei; Chen Zhang-long; Tu Shi-hang;
Computer Supported Cooperative Work in Design, 2004. Proceedings. The 8th
Conference on
Volume 1, 26-28 May 2004 Page(s):462 - 468 Vol.1
[AbstractPlus](#) | Full Text: [PDF](#)(675 KB) IEEE CNF

- ☐ **15. Implementing and optimizing real-time Java**
Corsaro, A.; Cytron, R.K.;
Parallel and Distributed Processing Symposium, 2003. Proceedings. Internatic
22-26 April 2003 Page(s):4 pp.
Digital Object Identifier 10.1109/IPDPS.2003.1213228
[AbstractPlus](#) | Full Text: [PDF](#)(189 KB) IEEE CNF

- ☐ **16. The object behavior of Java object-oriented database management syste**
Lo, C.-T.D.; Chang, M.; Frieder, O.; Grossman, D.;
Information Technology: Coding and Computing, 2002. Proceedings. Internatic
on
8-10 April 2002 Page(s):247 - 252
Digital Object Identifier 10.1109/ITCC.2002.1000395
[AbstractPlus](#) | Full Text: [PDF](#)(291 KB) IEEE CNF

- ☐ **17. A study on a garbage collector for embedded applications**
Krapf, R.C.; de Mattos, J.C.B.; Spellmeier, G.; Carro, L.;
Integrated Circuits and Systems Design, 2002. Proceedings. 15th Symposium
9-14 Sept. 2002 Page(s):127 - 132
Digital Object Identifier 10.1109/SBCCI.2002.1137648
[AbstractPlus](#) | Full Text: [PDF](#)(251 KB) IEEE CNF

- ☐ **18. Analyzing the performance of memory management in RTSJ**
Higuera-Toledano, M.T.; Issarny, V.;
Object-Oriented Real-Time Distributed Computing, 2002. (ISORC 2002). Proce
IEEE International Symposium on
29 April-1 May 2002 Page(s):26 - 33
Digital Object Identifier 10.1109/ISORC.2002.1003657
[AbstractPlus](#) | Full Text: [PDF](#)(331 KB) IEEE CNF

- ☐ **19. An analysis of the garbage collection performance in Sun's HotSpot™ J
Machine**
Dykstra, L.; Srisa-an, W.; Chang, J.M.;
Performance, Computing, and Communications Conference, 2002. 21st IEEE
3-5 April 2002 Page(s):335 - 339
Digital Object Identifier 10.1109/IPCCC.2002.995167
[AbstractPlus](#) | Full Text: [PDF](#)(563 KB) IEEE CNF

- ☐ **20. A performance comparison between stop-the-world and multithreaded co
generational garbage collection for Java**
Lo, C.-T.D.; Srisa-an, W.; Chang, J.M.;
Performance, Computing, and Communications Conference, 2002. 21st IEEE
3-5 April 2002 Page(s):301 - 308
Digital Object Identifier 10.1109/IPCCC.2002.995163
[AbstractPlus](#) | Full Text: [PDF](#)(748 KB) IEEE CNF

- ☐ **21. Deterministic Java in tiny embedded systems**
Nilsson, A.; Ekman, T.;
Object-Oriented Real-Time Distributed Computing, 2001. ISORC - 2001. Proce

IEEE International Symposium on
2-4 May 2001 Page(s):60 - 68
Digital Object Identifier 10.1109/ISORC.2001.922818
[AbstractPlus](#) | Full Text: [PDF\(708 KB\)](#) IEEE CNF

- ☐ **22. A performance analysis of the active memory system**
Witawas Srisa-An; Srisa-an; Chia-Tien Dan Lo; J Morris Chang;
Computer Design, 2001. ICCD 2001. Proceedings. 2001 International Confere
23-26 Sept. 2001 Page(s):493 - 496
Digital Object Identifier 10.1109/ICCD.2001.955073
[AbstractPlus](#) | Full Text: [PDF\(344 KB\)](#) IEEE CNF

- ☐ **23. The case for Java as a programming language**
Van Hoff, A.;
Internet Computing, IEEE
Volume 1, Issue 1, Jan.-Feb. 1997 Page(s):51 - 56
Digital Object Identifier 10.1109/4236.585172
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(200 KB\)](#) IEEE JNL

- ☐ **24. EarlGray: a component-based Java virtual machine for embedded system**
Ishikawa, H.; Nakajima, T.;
Object-Oriented Real-Time Distributed Computing, 2005. ISORC 2005. Eighth
International Symposium on
18-20 May 2005 Page(s):403 - 409
Digital Object Identifier 10.1109/ISORC.2005.26
[AbstractPlus](#) | Full Text: [PDF\(168 KB\)](#) IEEE CNF

- ☐ **25. An evaluation of aspect-oriented programming for Java-based real-time s
development**
Shiu Lun Tsang; Clarke, S.; Baniassad, E.;
Object-Oriented Real-Time Distributed Computing, 2004. Proceedings. Sevent
International Symposium on
2004 Page(s):291 - 300
Digital Object Identifier 10.1109/ISORC.2004.1300391
[AbstractPlus](#) | Full Text: [PDF\(1386 KB\)](#) IEEE CNF

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IEEE JNL IEEE Journal or Magazine

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IEE CNF IEE Conference Proceeding

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- ☐ 1. **Proposal for a field-evolvable hardware based on a microprocessor incor memory**
Sato, Y.;
Evolutionary Computation, 2001. Proceedings of the 2001 Congress on Volume 1, 27-30 May 2001 Page(s):608 - 615 vol. 1
Digital Object Identifier 10.1109/CEC.2001.934447
[AbstractPlus](#) | Full Text: [PDF](#)(524 KB) IEEE CNF
- ☐ 2. **Reducing memory requirement of cell state space based fuzzy logic cont approaches using k-d trees**
Feijun Song; Smith, S.M.; Rizk, C.G.;
Systems, Man, and Cybernetics, 1999. IEEE SMC '99 Conference Proceeding International Conference on Volume 6, 12-15 Oct. 1999 Page(s):144 - 149 vol.6
Digital Object Identifier 10.1109/ICSMC.1999.816480
[AbstractPlus](#) | Full Text: [PDF](#)(436 KB) IEEE CNF
- ☐ 3. **Customising parallelism and caching for machine learning**
Fidjeland, A.; Luk, W.;
Field-Programmable Technology (FPT), 2003. Proceedings. 2003 IEEE Internat Conference on 15-17 Dec. 2003 Page(s):204 - 211
Digital Object Identifier 10.1109/FPT.2003.1275749
[AbstractPlus](#) | Full Text: [PDF](#)(596 KB) IEEE CNF
- ☐ 4. **Functional abilities of a stochastic logic neural network**
Kondo, Y.; Sawada, Y.;
Neural Networks, IEEE Transactions on Volume 3, Issue 3, May 1992 Page(s):434 - 443
Digital Object Identifier 10.1109/72.129416
[AbstractPlus](#) | Full Text: [PDF](#)(684 KB) IEEE JNL
- ☐ 5. **An experiment in automatic modeling an electrical drive system using fu**
Costa Branco, P.J.; Dente, J.A.;
Systems, Man and Cybernetics, Part C, IEEE Transactions on Volume 28, Issue 2, May 1998 Page(s):254 - 262
Digital Object Identifier 10.1109/5326.669562
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(216 KB) IEEE JNL

- ☐ **6. Proceedings of the 26th Euromicro Conference. EUROMICRO 2000. Inform the Future**
Optical Fiber Communication Conference, 2000
Volume 4, 7-10 March 2000
Digital Object Identifier 10.1109/OFC.2000.869490
[AbstractPlus](#) | Full Text: [PDF](#)(336 KB) IEEE CNF

- ☐ **7. Fuzzy neurons: a coarse-grained reconfigurable element for computation**
Czezowski, P.J.; Hart Poskar, C.; Corbett, F.D.; McLeod, R.D.;
Electrical and Computer Engineering, 1999 IEEE Canadian Conference on
Volume 2, 9-12 May 1999 Page(s):1074 - 1080 vol.2
Digital Object Identifier 10.1109/CCECE.1999.808197
[AbstractPlus](#) | Full Text: [PDF](#)(520 KB) IEEE CNF

- ☐ **8. Proceedings of the 32nd Annual Hawaii International Conference on System Sciences, 1999. HICSS-32. Abstracts and CD-ROM of Full Papers**
System Sciences, 1999. HICSS-32. Proceedings of the 32nd Annual Hawaii International Conference on
Volume Track1, 5-8 Jan. 1999
Digital Object Identifier 10.1109/HICSS.1999.772649
[AbstractPlus](#) | Full Text: [PDF](#)(80 KB) IEEE CNF

- ☐ **9. Self-organising fuzzy decision trees for robot navigation: An online learn**
Shah Hamzei, G.H.; Mulvaney, D.J.;
Systems, Man, and Cybernetics, 1998. 1998 IEEE International Conference on
Volume 3, 11-14 Oct. 1998 Page(s):2332 - 2337 vol.3
Digital Object Identifier 10.1109/ICSMC.1998.725004
[AbstractPlus](#) | Full Text: [PDF](#)(888 KB) IEEE CNF

- ☐ **10. A learning fuzzy decision tree and its application to tactile image**
Han-Pang Huang; Chao-Chiun Liang;
Intelligent Robots and Systems, 1998. Proceedings., 1998 IEEE/RSJ International Conference on
Volume 3, 13-17 Oct. 1998 Page(s):1578 - 1583 vol.3
Digital Object Identifier 10.1109/IROS.1998.724823
[AbstractPlus](#) | Full Text: [PDF](#)(500 KB) IEEE CNF

- ☐ **11. Proceedings of the Thirty-First Hawaii International Conference on System Sciences, 1998., Proceedings of the Thirty-First Hawaii International Conference on System Sciences, 1998., Proceedings of the Thirty-First Hawaii International Conference on System Sciences, 1998.**
Volume 2, 6-9 Jan. 1998
Digital Object Identifier 10.1109/HICSS.1998.651675
[AbstractPlus](#) | Full Text: [PDF](#)(1576 KB) IEEE CNF

- ☐ **12. Proceedings of the Thirtieth Hawaii International Conference on System Sciences, 1997, Proceedings of the Thirtieth Hawaii International Conference on System Sciences, 1997, Proceedings of the Thirtieth Hawaii International Conference on System Sciences, 1997.**
Volume 1, 7-10 Jan. 1997
Digital Object Identifier 10.1109/HICSS.1997.667167
[AbstractPlus](#) | Full Text: [PDF](#)(500 KB) IEEE CNF

- ☐ **13. Yield management in microelectronic manufacturing**
El-Kareh, B.; Ghatalia, A.; Satya, A.V.S.;
Electronic Components and Technology Conference, 1995. Proceedings., 45th
21-24 May 1995 Page(s):58 - 63
Digital Object Identifier 10.1109/ECTC.1995.514362
[AbstractPlus](#) | Full Text: [PDF](#)(524 KB) IEEE CNF

- ☐ **14. Magnetic neurochip: structure and logic elements**
Red'ko, V.G.; Skidanov, V.A.;
Neuroinformatics and Neurocomputers, 1995., Second International Symposium
20-23 Sept. 1995 Page(s):353 - 360
Digital Object Identifier 10.1109/ISNINC.1995.480880
[AbstractPlus](#) | Full Text: [PDF\(344 KB\)](#) IEEE CNF

- ☐ **15. A stochastic logic neural network as a deterministic and probabilistic Ho**
Kondo, Y.; Sawada, Y.;
Neural Networks, 1991., IJCNN-91-Seattle International Joint Conference on
Volume ii, 8-14 July 1991 Page(s):924 vol.2
Digital Object Identifier 10.1109/IJCNN.1991.155578
[AbstractPlus](#) | Full Text: [PDF\(72 KB\)](#) IEEE CNF

- ☐ **16. CAM-based ASOCS implementation**
Bartczak, A.; Daly, J.;
Neural Networks, 1994. IEEE World Congress on Computational Intelligence.,
International Conference on
Volume 4, 27 June-2 July 1994 Page(s):2103 - 2107 vol.4
Digital Object Identifier 10.1109/ICNN.1994.374539
[AbstractPlus](#) | Full Text: [PDF\(336 KB\)](#) IEEE CNF

- ☐ **17. Implementation of the PIPE processor**
Farrens, M.K.; Pleszhun, A.R.;
Computer
Volume 24, Issue 1, Jan. 1991 Page(s):65 - 70
Digital Object Identifier 10.1109/2.67195
[AbstractPlus](#) | Full Text: [PDF\(504 KB\)](#) IEEE JNL

- ☐ **18. Gait event detection for FES using accelerometers and supervised mach**
Williamson, R.; Andrews, B.J.;
Rehabilitation Engineering, IEEE Transactions on [see also IEEE Trans. on Ne
Rehabilitation]
Volume 8, Issue 3, Sept. 2000 Page(s):312 - 319
Digital Object Identifier 10.1109/86.867873
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(260 KB\)](#) IEEE JNL

- ☐ **19. Design and development paradigm for industrial formal verification CAD**
Krishnamurthy, N.; Abadir, M.S.; Martin, A.K.; Abraham, J.A.;
Design & Test of Computers, IEEE
Volume 18, Issue 4, July-Aug. 2001 Page(s):26 - 35
Digital Object Identifier 10.1109/54.936246
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(248 KB\)](#) IEEE JNL

- ☐ **20. An undergraduate system-on-chip (SoC) course for computer engineerin**
Bindal, A.; Mann, S.; Ahmed, B.N.; Raimundo, L.A.;
Education, IEEE Transactions on
Volume 48, Issue 2, May 2005 Page(s):279 - 289
Digital Object Identifier 10.1109/TE.2004.842911
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1016 KB\)](#) IEEE JNL

- ☐ **21. Identification of chaotic system using recurrent compensatory neuro-fuz**
Cheng-Hung Chen; Chin-Teng Lin;
Cellular Neural Networks and Their Applications, 2005 9th International Works
28-30 May 2005 Page(s):15 - 18
Digital Object Identifier 10.1109/CNNA.2005.1543149
[AbstractPlus](#) | Full Text: [PDF\(760 KB\)](#) IEEE CNF

- ☐ **22. Extended Forward Implications and Dual Recurrence Relations to Identify Untestable Faults**
Syal, M.; Arora, R.; Hsiao, M.S.;
Computer Design, 2005. Proceedings. 2005 International Conference on
02-05 Oct. 2005 Page(s):453 - 460
Digital Object Identifier 10.1109/ICCD.2005.53
[AbstractPlus](#) | Full Text: [PDF](#)(336 KB) IEEE CNF
- ☐ **23. Nonvolatile circuits as building blocks**
Salsbury, P.;
Solid-State Circuits Conference. Digest of Technical Papers. 1985 IEEE Intern
Volume XXVIII, Feb 1985 Page(s):116 - 117
[AbstractPlus](#) | Full Text: [PDF](#)(336 KB) IEEE CNF
- ☐ **24. Research on precautions against data disaster of logic security smart card**
Ming-Sheng Liu; Hui Liu; Yin-Hua Ma; Wen-Xiong Li;
Machine Learning and Cybernetics, 2004. Proceedings of 2004 International C
Volume 5, 26-29 Aug. 2004 Page(s):2753 - 2756 vol.5
Digital Object Identifier 10.1109/ICMLC.2004.1378337
[AbstractPlus](#) | Full Text: [PDF](#)(669 KB) IEEE CNF
- ☐ **25. Intelligent agents in virtual worlds**
Iglesias, A.; Luengo, F.;
Cyberworlds, 2004 International Conference on
18-20 Nov. 2004 Page(s):62 - 69
Digital Object Identifier 10.1109/CW.2004.40
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